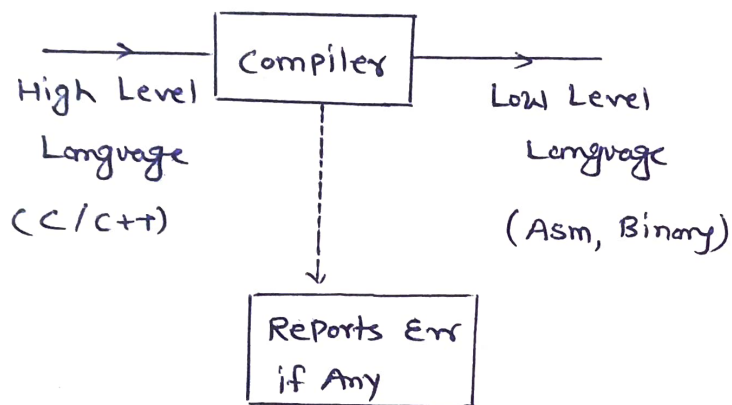
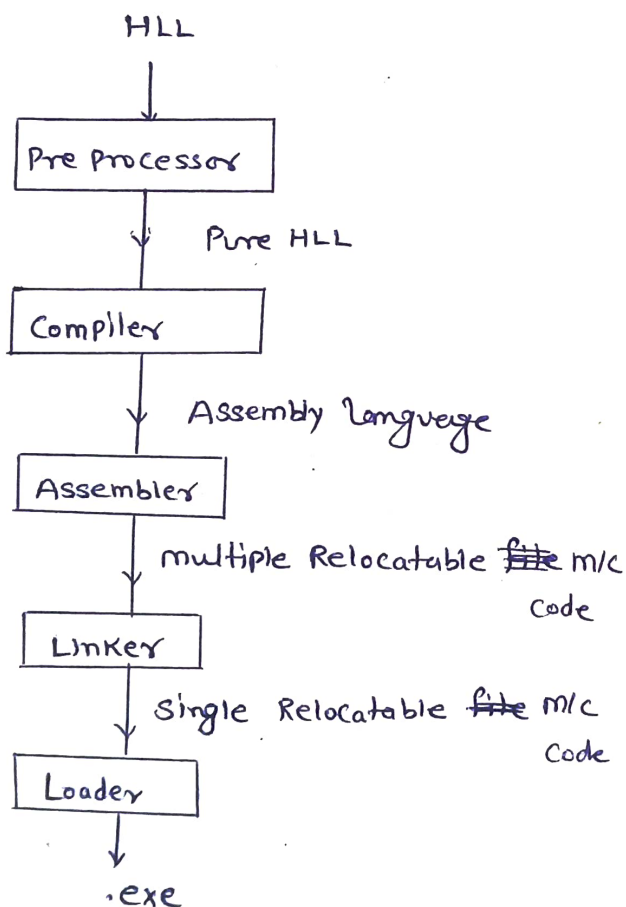


* Compiler Design *

→ A Compiler is a Software that translates Computer code written in one Programming Language (source) into Another (target).



* Language Processing system -



• Pre processor -

→ Removes All #include directives by including the files

↳ Called file inclusion

→ Remove all #define directives using macro expansion.

• Compiler -

→ verifies All Limits, Ranges, Errors etc
→ slower than other system software
→ occupies huge Amount of memory space.

• Assembler -

→ for Every platform (Hardware + OS) we have an assembler. They are not universal.

→ The output of Assembler is called object file. It translates Assembly Language to machine code.

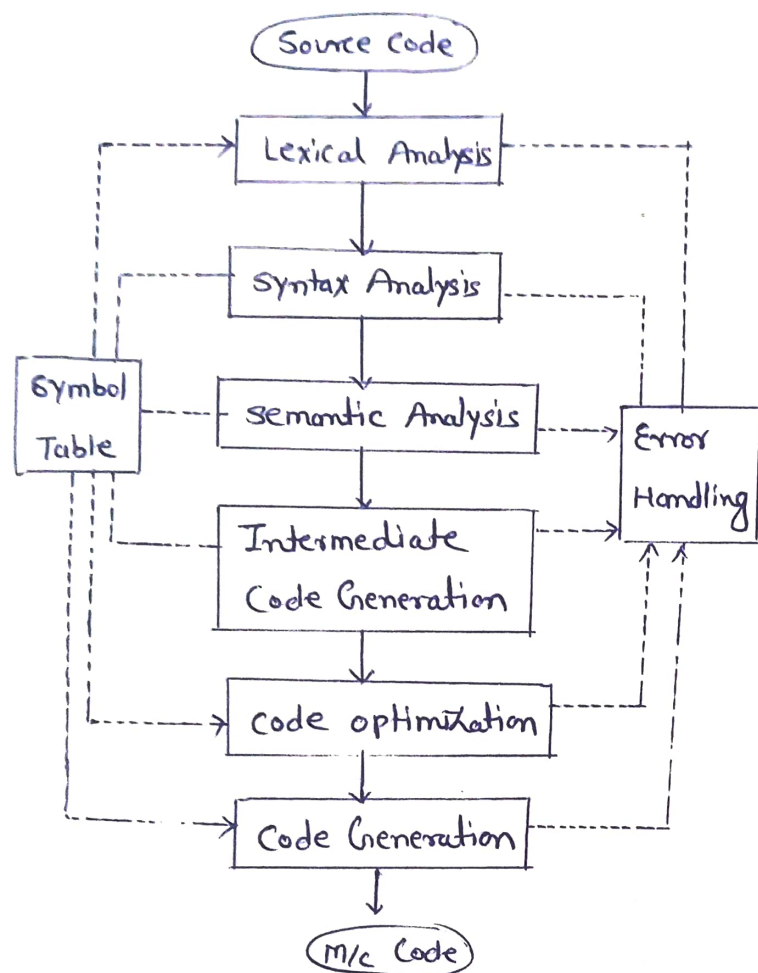
• Linker :-

↳ Basic Work is to merge the object codes (that have not been connected), produced by Compiler, Assembler, Standard Library fn and OS Resources.

• Loader :-

↳ find/calculate the exact address of ~~these~~ memory locations to store code.

* Phase of Compilers-



1) Lexical Analyzer:-

→ Reads the program and Convert it into a sequence of tokens.

↓
A smallest unit of meaningful data

→ These Tokens Can ~~be~~ represent keywords, identifiers, Constants, operators.

→ It removes white spaces and comments.

float x, y, z;

x = y + z * 60

x - token : identifier

= : operator

y : identifier

+ : operator

z : identifier

* : operator

60 : Constant

2) Syntax Analyzer - (Parser)

→ It checks Whether the code adheres to the language rules.

→ We Construct Parse / Syntax Tree.

→ Uses productions of CFG to Construct the parse Tree.

→ It reads all the tokens one by one.

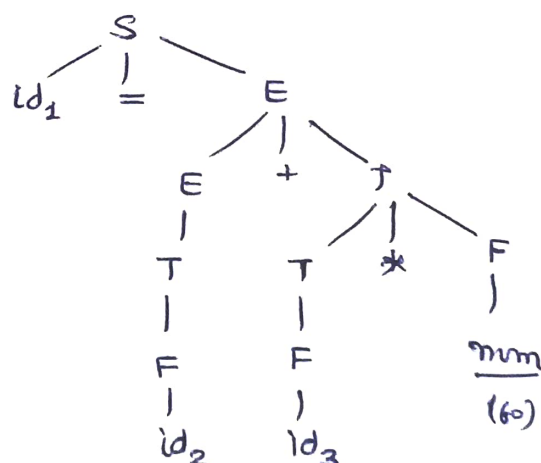
$S \rightarrow Id = E$

$E \rightarrow E + T \mid T$

$T \rightarrow T * F \mid F$

$F \rightarrow id \mid num$

$\Rightarrow id_1 = id_2 + id_3 * 60$



3) Semantic Analyzer -

- It ~~ver~~ verifies the parse tree whether it's meaningful or not.
- It checks whether the program has any semantic errors, such as type mismatched and undeclared variables.
- o/p : ~~Anno~~ Annotated Parse tree

4) Intermediate code Generator -

- Generate Intermediate code.
- It is not specific to any particular m/c, making it portable and easier to optimize.

⊕ : 1) Platform Independence

2) Simplifying optimization

- Dead Code Elimination
- Loop optimization
- Common subexpression

elimination

↳ Reusing previously calculated values to avoid redundant calculations

⌈ $x = y + z * 60$

$t_1 = z * 60$

$t_2 = t_1 + y$

$x = t_2$

5) Code optimizer

- This is a process of improving the intermediate ~~to~~ or target code, to make the program run faster, use less resources.
- optimization is classified broadly into 2 types -
 - machine Independent
 - machine Dependent

6) Code Generation

- final phase of compiler, where the intermediate representation of source program (e.g. - Three Address Code) is translated into machine code.

• Symbol Table -

↳ It is a data structure being used and maintained by the compiler, consists all the identifier's name along with their types.

- Information stored in the symbol table about identifier -

name
type
scope
size
offset

• Error handler

→ It is a sub-routine to take care of the Continuation of Compilation, even any error at any phase occur.

→ Compiler Can handle three types of Err -

- 1) Lexical Errors
- 2) Syntax Errors
- 3) Semantic Errors

• Passes :- A Pass refers to the traversal of Compiler through the Entire Program.

1) Single Pass Compiler -

- Reads and processes the source code in a single Pass, translating it directly into machine code.
- faster than multi pass Compiler
- memory efficient - use less memory
- Limited optimization

 - Tiny C Compiler (TCC)
Pascal

2) Multi-Pass Compiler -

- It processes the source code multiple times before producing the final output.
- Advanced optimization
- Context Awareness
- memory usage ↑
- Gcc, LLVM

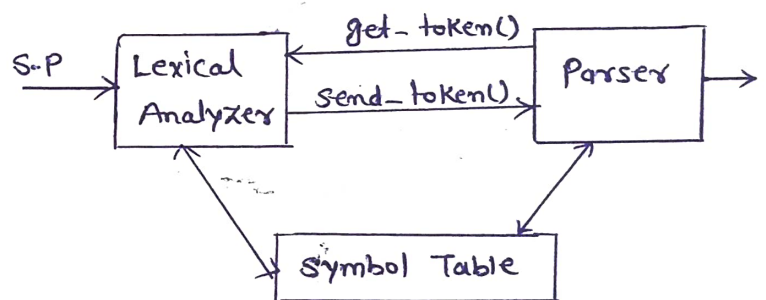
• Cross Compiler -

A Cross Compiler is a Compiler Capable of creating executable code for a platform other than the one on which the Compiler is running.

↳ A Cross Compiler executes on machine X and produce machine code for machine Y.

* Lexical Analyzer -

→ Lexical Analysis, lexing or tokenization is the process of converting a sequence of characters into a sequence of tokens.



→ Actual Representation or stream of characters is called as Lexemes; and Logical meaning of that lexemes is known as Tokens.

Lexeme	Token
while	While
(	LParen
γ	identifier
>=	Comparison
3	Integer
=	Assignment

→ Lexing Can be divided into 2 stages:

A) Scanning : Segments the input string into syntactic units called lexemes and categorizes these into token classes

B) Evaluating: Convert lexemes into processed values.

→ Tokens are normally identifiers, keywords, operators, constant and ~~special~~ special symbols.

→ Lexical Analyzer uses a DFA to recognize these tokens, AS DFAs are designed to identify Regular Expression.

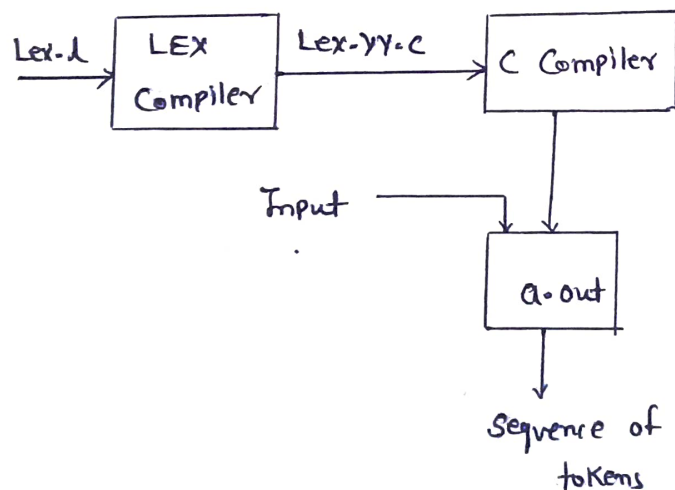
Lexical Analysis phase Can be Automated using a tool Called Lex.

- LEX is a Unix utility

- LEX Compiler -

Takes a specification file (with extension .l) containing Regular expressions and actions.

Produce lex.yy.c file.



→ secondary function of Lexical Analyzer

- 1) Removal of Comments lines
- 2) Removal of White space
- 3) Give Error msg Along with line No. and Col No.

* Ambiguous Grammar

→ The Grammar CFG is said to be ambiguous if there are more than one derivation tree for any string i.e. if there exist more than one derivation tree (LMDT or RMDT).

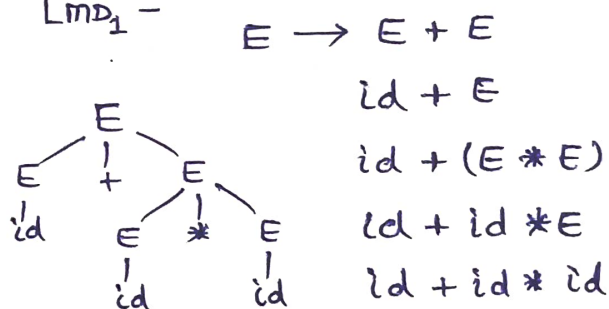
$E \rightarrow E + E \mid E * E \mid id$

$V = \{E\}$

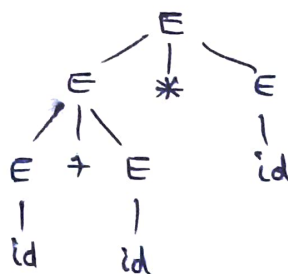
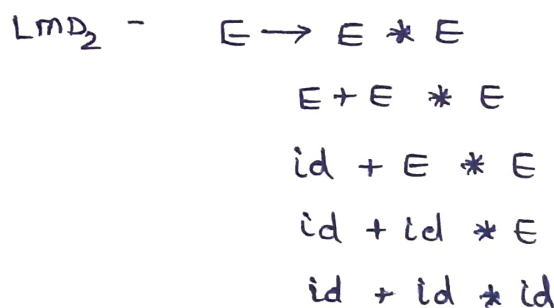
$T = \{+, *, id\}$

str = id + id * id

LMD₁ -



LMD₂ -



• Ambiguous to UnAmbiguous Grammar -

$$E \rightarrow E + E \mid E * E \mid id$$

$$W = id + id * id$$

→ Removal of Ambiguity -

1) Precedence -

if different operators are used, we will consider the precedence of the operators.

- The level at which the production is present denotes the priority of operators.

• high levels $\equiv$ Less priority
 $\downarrow$
 Close to Roots

Lower levels $\equiv$ High priority

2) Associativity -

If the same precedence operators are in production, then we will have to consider the associativity.

$(+, -) \rightarrow$ Left to Right Associative

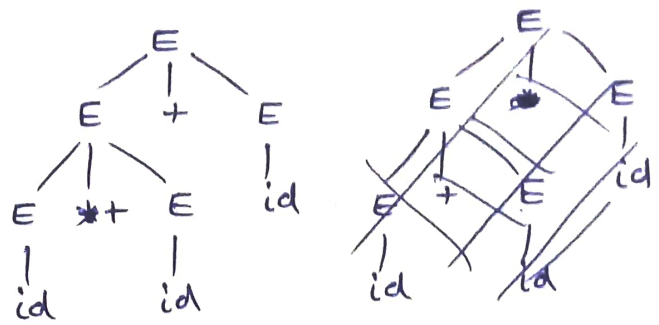
$(*, /) \rightarrow$ Left to Right Associative

$\downarrow$
 Left Recursion

$(\wedge) \rightarrow$ Right to Left Associative

$$E \rightarrow E + E \mid E * E \mid id$$

$$W = id + id * id$$



According to Associativity, for

$$W = (id + id) + id$$

tree I is correct.

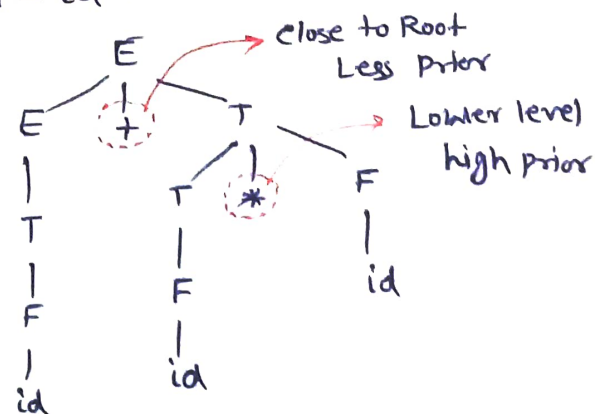
→ We have to break right recursion.

$$E \rightarrow E + id \mid id \quad (LR)$$

$$\Rightarrow E \rightarrow E + E \mid E * E \mid id$$

$$\left\{ \begin{array}{l} E \rightarrow E + T \mid T \\ T \rightarrow T * F \mid F \\ F \rightarrow id \end{array} \right.$$

$$W = id + id * id$$



Left Recursion : $A \rightarrow A\alpha / \beta$

Right Recursion : $A \rightarrow \alpha A / \beta$

• Elimination of left Recursion-

$$A \rightarrow A\alpha / \beta$$

$$\equiv \beta\alpha^*$$

$$\downarrow$$

$$\left[\begin{array}{l} A \rightarrow \beta A' \\ A' \rightarrow \epsilon / \alpha A' \end{array} \right] \equiv \beta\alpha^*$$

$$1) E \rightarrow E \underset{\alpha}{+T} / \underset{\beta}{T}$$

$$\left[\begin{array}{l} E \rightarrow TE' \\ E' \rightarrow \epsilon / +TE' \end{array} \right]$$

$$2) S \rightarrow S\alpha / S\beta\gamma\delta$$

$$S \rightarrow \underset{\alpha}{S\beta\gamma\delta} / \underset{\beta}{\delta}$$

$$S \rightarrow \delta S'$$

$$S' \rightarrow \epsilon / \alpha S\beta\gamma\delta$$

$$A \rightarrow A\alpha / \beta_1 / \beta_2 / \dots / \beta_n$$

$$\downarrow$$

$$\left[\begin{array}{l} A \rightarrow \beta_1 A' / \beta_2 A' / \dots / \beta_n A' \\ A' \rightarrow \epsilon / \alpha A' \end{array} \right]$$

$$A \rightarrow A\alpha_1 / A\alpha_2 / \dots / A\alpha_n / \beta$$

$$\downarrow$$

$$\left[\begin{array}{l} A \rightarrow \beta A' \\ A' \rightarrow \alpha_1 A' / \alpha_2 A' / \alpha_3 A' / \dots / \alpha_n A' / \epsilon \end{array} \right]$$

$$A \rightarrow A\alpha_1 / A\alpha_2 / \dots / A\alpha_n / \beta_1 / \beta_2 / \dots / \beta_m$$

$\downarrow$

$$\left[\begin{array}{l} A \rightarrow \beta_1 A' / \beta_2 A' / \dots / \beta_m A' \\ A' \rightarrow \alpha_1 A' / \alpha_2 A' / \dots / \alpha_n A' / \epsilon \end{array} \right]$$

• Left factoring:-

Process of Conversion of Non deterministic grammar into deterministic grammar is known as left factoring

$$A \rightarrow \alpha\beta_1 / \alpha\beta_2 / \alpha\beta_3 / \alpha\beta_4$$

α is a Common prefix

$$\left[\begin{array}{l} A \rightarrow \alpha A' \\ A' \rightarrow \beta_1 / \beta_2 / \beta_3 / \beta_4 \end{array} \right]$$

$$1) S \rightarrow iEts / iEtses / a$$

$$E \rightarrow b$$

$\downarrow$

$$\left[\begin{array}{l} S \rightarrow iEts S' / a \\ S' \rightarrow \epsilon / es \\ E \rightarrow b \end{array} \right]$$

$$2) S \rightarrow a s s b s / a s a s b / a b b / b^*$$

$$S \rightarrow a S' / b$$

$$\left[\begin{array}{l} S' \rightarrow s s b s / s a s b / b b \\ \left[\begin{array}{l} S' \rightarrow s s'' / b b \\ S'' \rightarrow s b s / a s b \end{array} \right] \end{array} \right]$$

$$1) S \rightarrow a s / b$$

$$S' \rightarrow s S' / b b$$

$$S'' \rightarrow s b s / a s b$$