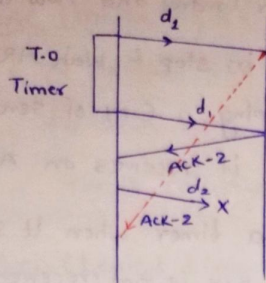


3.) Delay Ack:



→ We solve this problem by assigning Next Expected frame Number With Ack.

$$\eta = \frac{T_T}{T_T + 2T_P}$$

→ Total Waiting time = Stop & Wait Time + Sender time out + Receiver time out

$$\eta = \frac{1}{1 + 2a}$$

$$\text{Throughput} = \eta \times BW$$

• Go Back - N ARQ :-

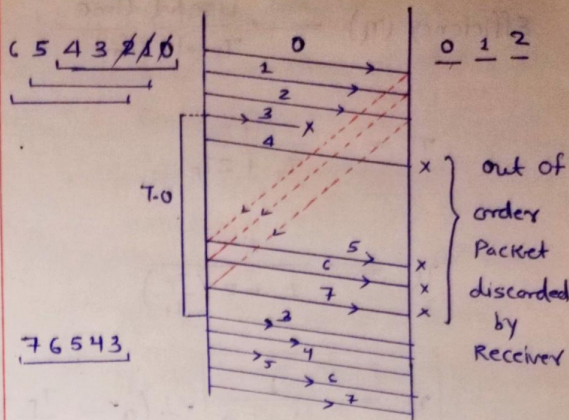
→ In the sliding window Concept instead of sending one packet and wait for the ack, we send w packet and wait for the Ack.

[w: sender window size]

→ GB-N (N > 1)

1) Sender window size = N

2) Receiver window size = 1



Note :-

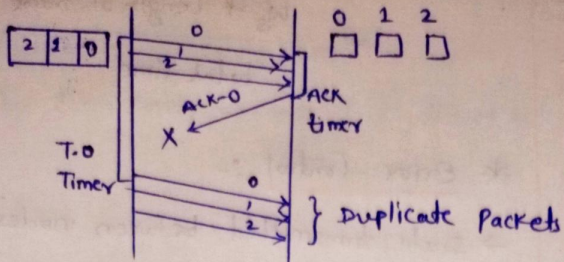
- 1) out of order packet is not Received by the receiver.
- 2) Timer is maintained only for the first frame (Right most) in the window because If its timer expire then sender assume that rest of the frames are Not received by receiver (Because out of order delivery is rejected).

Note :-

- 1) GB-N uses Commulative Ack and ACK Number defines the Number of next expected frame.

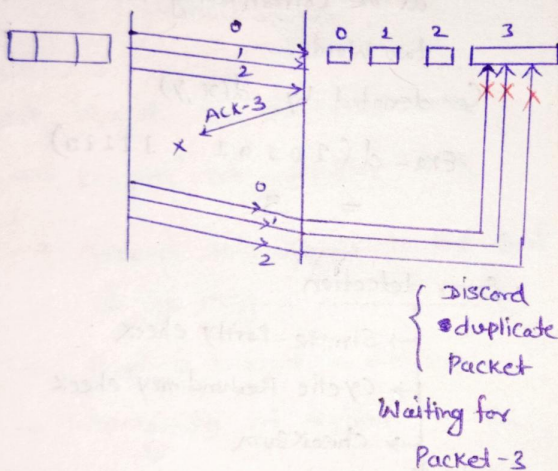
ACK timer < T.O. Timer

• Relationship b/w Window size & seq. No.



→ Duplicate packet Problem Can be solved by Increasing the seq. No. or Applying this formula—

$$W_S + W_R \leq \text{ASN} \downarrow \text{Available Sequence Number}$$



Using formula— $W_S + W_R \leq \text{ASN}$

$$3 + 1 \leq 4$$

$$4 \leq 4 \checkmark$$

$$\Rightarrow W_S \leq \text{ASN} - 1$$

→ if seq No. = K bit

$$\text{Total seq. No.} = 2^K$$

$$\begin{matrix} W_S & W_R \\ \hline 2^K - 1 & 1 \end{matrix}$$

$$\Rightarrow \eta = \frac{\text{useful time}}{\text{total time}}$$

$$= \frac{N * T_d(\text{frame})}{T_d(\text{frame}) + 2 * P_d + P_{rd} + T_d(\text{ACK})}$$

$$= \frac{N}{1 + 2a}, \quad a = \frac{P_d}{T_d}$$

$$\Rightarrow \text{Throughput} = \eta \times \text{BW}$$

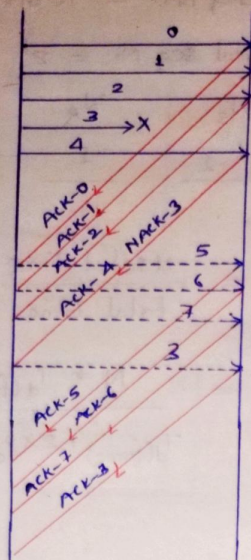
OR

$$\text{Throughput} = \frac{N * \text{Length of frame}}{\text{Total Time}}$$

* Selective Repeat ARQ :-

- In SR — $W_R = W_S$
- SR Protocol uses Independent ACK. and ACK Number defines Number of Error free packet Received.
- It Can Receive out of order packet but packets are delivered to upper layer in order.
- Searching and Sorting Algorithm required. searching is done by sender. sorting is done by receiver.
- Timer is maintained for Each and Every frame in the Window at sender side.

~~43210~~
~~54321~~
~~65432~~
~~76543~~



0	1	2	3	4
5	6	7		
8	9	10	11	12
13	14	15	16	17

Sort these
 According to
 Packet No.
 then send to
 Network Layer
 and delete
 Window

$$\text{ThroughPut} = \eta \times Bw$$

$$= \frac{W_s * \text{Length of frame}}{\text{total time}}$$

* Error - Control :-

→ Data transmitted between nodes can sometimes be altered or corrupted during transmission.

→ This corruption can manifest as either a single-bit error or burst error.

↳ (multiple bit change)

• Hamming distance :-

↳ Total Number of dissimilar bits at the corresponding positions in two words.

↳ denoted by $d(x, y)$

Exa - $d(10101, 11110)$
 $= 3$

• Error detection

- Simple Parity check
- Cyclic Redundancy check
- checksum

• Error Correction

↳ Hamming Code

→ For 1st out of order delivery or if Packet received is corrupted then NACK for respective packet is sent by receiver to sender.

⇒ Relationship b/w window size and sequence No.

• Duplicate packet problem can be solved by increasing the sequence No. or decreasing the sender window size.

$$W_s + W_R \leq A.S.N$$

• if seq No. - K bit

$$\frac{2^{K-1}}{W_s} \quad \frac{2^{K-1}}{W_R}$$

$$\eta = \frac{W_s * T_d(\text{frame})}{T_d(\text{frame}) + 2P_d + Q_d + P_R + T_d(\text{ack})}$$

• Simple Parity check / Single Parity check

DataWord $\rightarrow$ m bits

CodeWord $\rightarrow$ $(m+1)$ bits

for Even Parity $\rightarrow$ No. of 1s should be Even.

Exa-
 $1010 \rightarrow 10100$
 $1110 \rightarrow 11101$

• Cyclic Redundancy Check (CRC)

- $\rightarrow$ If message has m bits then transmitter sends $(m+r)$ bits, r represents redundant bits.
- $\rightarrow$ This Can detect all odd Errors, single bit, burst Error of length Equal to polynomial degree

$\rightarrow$ Length of dataword $= n$

$\rightarrow$ Length of divisor $= K$

$\rightarrow$ Append $K-1$ Zero's to the end of the msg.

$\rightarrow$ Perform modulo 2 division

$\rightarrow$ Remainder of division $=$ CRC

$\rightarrow$ Code Word $= (n + K - 1)$ bits

CodeWord = dataword with Appended Zero
 $+$
 CRC

Exa- Data = 1001001

Divisor = 1101

$$\begin{array}{r}
 1101 \overline{) 1001001000} \\
 \underline{1101} \\
 0100001000 \\
 \underline{1101} \\
 010101000 \\
 \underline{1101} \\
 01111000 \\
 \underline{1101} \\
 0010000 \\
 \underline{1101} \\
 01010 \\
 \underline{1101} \\
 0111 \\
 \hline
 \text{CRC Remainder}
 \end{array}$$

CodeWord = 1001001111

• Algebraic Properties to choose the polynomial

1) It should not be divisible by x .

$\Rightarrow$ This Cond guarantee that all the burst Error upto the length equal to the length of polynomial are detected.

2) It should be divisible by $(x+1)$.

$\rightarrow$ To detect odd No. of Error

Note :-

1) CRC Can detect all single bit Error.

2) CRC Can detect all double bit Error if polynomial does not divided by $x^K + 1$ for any K .

3) CRC Can detect any odd No. Error if polynomial should be divisible by $x+1$.

4) CRC Can detect all burst Errors

if Polynomial $G(x)$ has degree n and is not divisible by x .

detects all burst Errors of length $\leq n$

5) CRC Can detect most of large bit Errors with high Probability.

* CheckSum :-

- It Works by adding ~~up~~ up all data units (Packet/Words), taking the Complement, and appending it as the checksum at the Sender side.
- ~~The receiver performs the same addition, and if the result is all 1's (no Error), Else an Error is detected.~~

Steps :-

1) Sender Side :-

- 1) m bit checksum
- 2) n bits in transmitting message
- 3) n bits are divided into fixed size segment equal to m bit
- 4) sum to all m bit segments
- 5) ~~one~~ 1's Complement of final output
- 6) output is called checksum

2) Receiver side -

- 1) Transmitted bits + checksum
↳ sum of m bit segments

- 2) 1's Complement

if Result = 0 No Err

Result $\neq$ 0 Err

Exa -

Sender Wants to transmit -

msg - 100 11001 11100010 00100100
10000100

1) sender -

Add them all -

$$\begin{array}{r} 10011001 \\ 11100010 \\ \hline 101111011 \\ \text{+ 1} \\ \hline 01111100 \\ + 00100100 \leftarrow \text{Next word} \\ \hline 10100000 \\ 10000100 \leftarrow \text{Next word} \\ \hline 100100100 \\ \text{+ 1} \\ \hline 00100101 \\ \text{1's } \hookrightarrow 11011010 \rightarrow \text{check sum} \end{array}$$

2) Receiver side

$$\begin{array}{r} 00100101 \\ 11011010 \\ \hline 11111111 \\ \text{1's } \hookrightarrow 00000000 \checkmark \text{ (No Err)} \end{array}$$